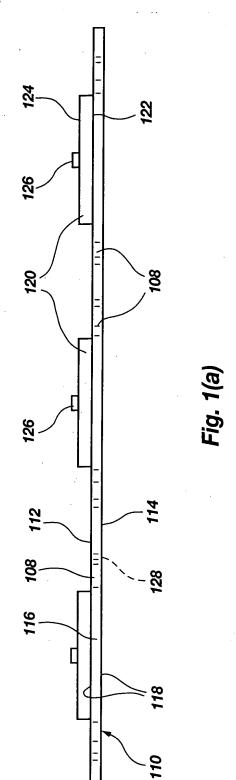
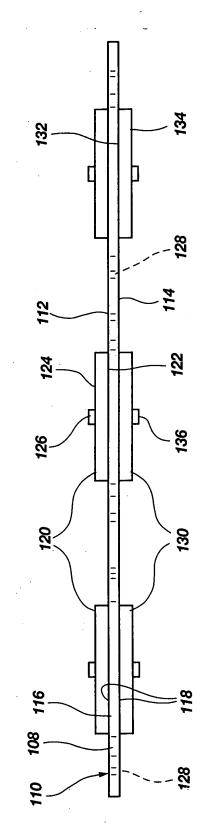
1/6



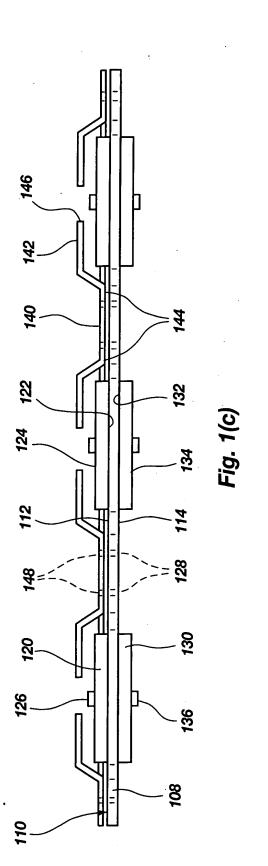
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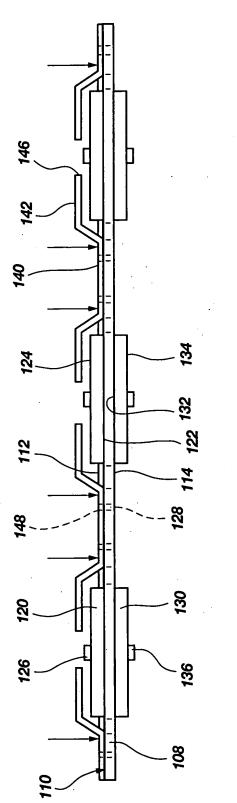




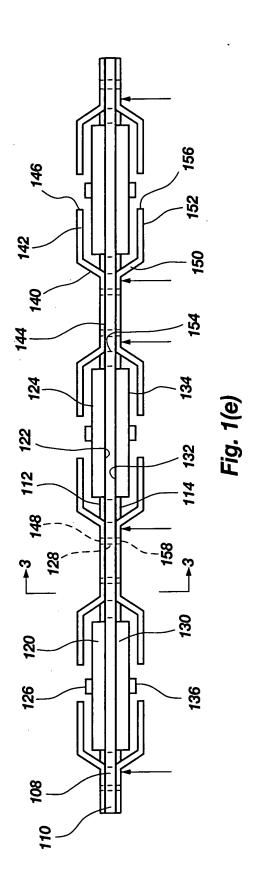
rig. 1(b)

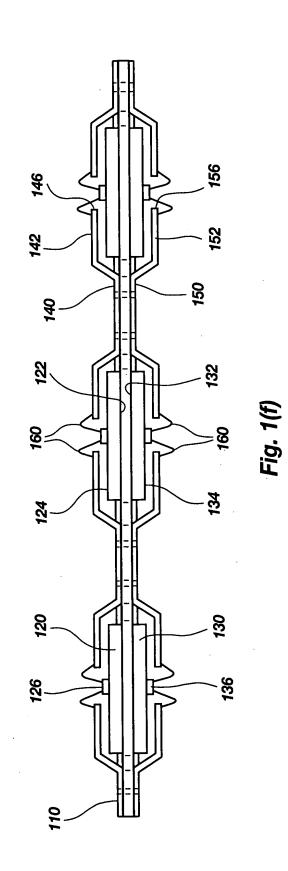




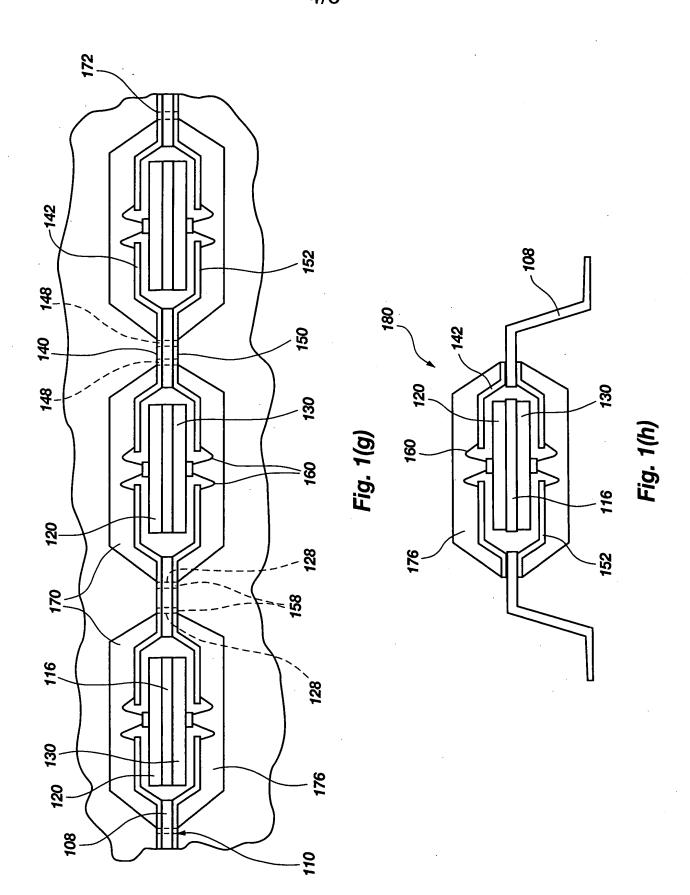


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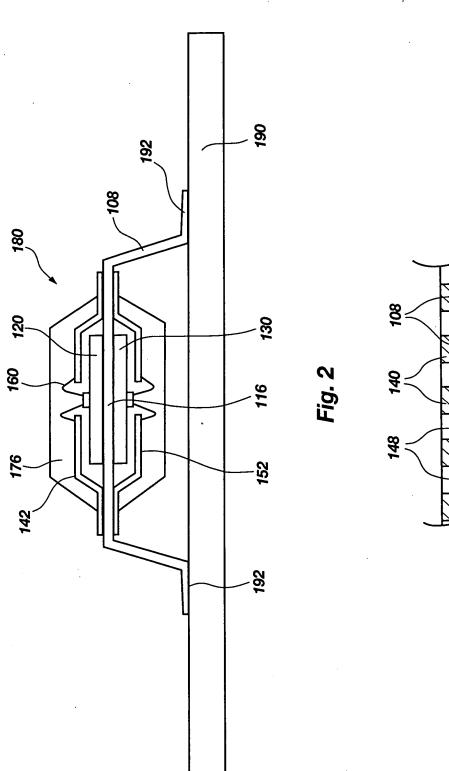












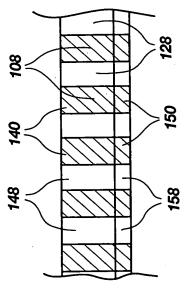


Fig. 3

TITLE: DUAL LOC SEMICONDUCTOR ASSEMBLY EMPLOYING FLOATING LEAD FINGER STRUCTURE Inventor: Venkateshwaran Vaiyapuri Serial No.: Not Yet Assigned Docket No.: 4369.1US

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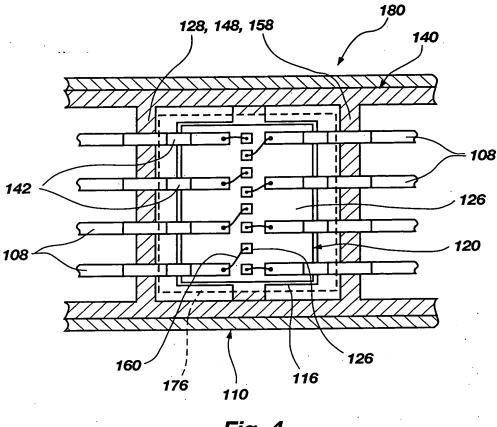


Fig. 4

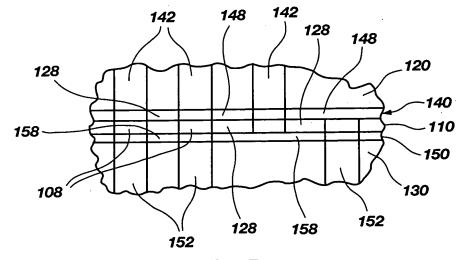


Fig. 5